

## CLAIMS

We claim:

1           1.     An electronic circuit arrangement, in particular an LED circuit arrangement  
2     (1), having a lead (3), via which electronic circuit elements (6) of the circuit  
3     arrangement, in particular LED components, can be driven by a drive circuit (2; 20; 21;  
4     22),

5           wherein

6           the lead (3) has a plurality of coding conductors (3c, 3d), which carry a code by  
7     means of a combination of electrically interrupted and electrically continuous coding  
8     conductors (3c, 3d), said code giving an indication of specific properties of the circuit  
9     arrangement.

1           2.     The electronic circuit arrangement as claimed in claim 1,

2           wherein

3           the code can be detected by means of an evaluation circuit (70; 71; 72; 73),  
4     which passes a corresponding control signal to the drive circuit (2; 20; 21).

1           3.     The electronic circuit arrangement as claimed in claim 1,

2           wherein

3           an interrupted coding conductor represents the logic state "0" and a  
4     non-interrupted coding conductor represents the logic state "1".

1        4.    The electronic circuit arrangement as claimed in claim 2,  
2        wherein  
3        at least two coding conductors (3d) can in each case be individually connected to  
4        a measurement voltage source of the drive circuit (21) and the coding conductors (3d)  
5        can furthermore be connected to the evaluation circuit (73).

1        5.    The electronic circuit arrangement as claimed in claim 2,  
2        wherein  
3        the evaluation circuit (72; 73) is a digital/analog converter (D/A).

1        6.    The electronic circuit arrangement as claimed in claim 5,  
2        wherein  
3        the digital/analog converter (D/A) contains a resistor network.

1        7.    The electronic circuit arrangement as claimed in claim 6,  
2        wherein  
3        a reference voltage ( $U_{ref}$ ) of the digital/analog converter (D/A) is a measurement  
4        voltage provided by the measurement voltage source.

1        8.    The electronic circuit arrangement as claimed in claim 1,  
2        wherein

3 an electrical supply line for the circuit elements can be provided by at least one  
4 electrically continuous coding conductor (3d).

1 9. The electronic circuit arrangement as claimed in claim 1,  
2 wherein  
3 the lead and the circuit arrangement are arranged on a common carrier, in  
4 particular on a common printed circuit board.

1 10. The electronic circuit arrangement as claimed in claim 1,  
2 wherein  
3 the lead is arranged on a flexible part of a carrier.

1 11. The electronic circuit arrangement as claimed in claim 1,  
2 wherein  
3 the coding conductors (3c, 3d) can be interrupted by perforation, stamping and/or  
4 milling or in a comparable manner.

1 12. The electronic circuit arrangement as claimed in claim 1,  
2 wherein  
3 the lead can be electrically connected to the drive circuit and/or to the circuit  
4 arrangement (1) by plug connectors.

1           13. The electronic circuit arrangement as claimed in claim 1,  
2           wherein  
3           the circuit arrangement (1) is an LED circuit arrangement (1).

1           14. The electronic circuit arrangement as claimed in claim 13,  
2           wherein  
3           the LED circuit arrangement (1) has a plurality of LED chains each having a  
4           plurality of LED components (6), said LED chains being electrically connected in parallel  
5           with one another.

1           15. The electronic circuit arrangement as claimed in claim 14,  
2           wherein  
3           the coding is correlated by the brightness grouping of the LED components used  
4           in the LED circuit arrangement.

1           16. A method for coding an electronic circuit arrangement, in particular an  
2           LED circuit arrangement, as claimed in claim 1,  
3           wherein  
4           the lead is coded by perforation, stamping and/or milling or in a comparable  
5           manner after the completion of the electronic circuit arrangement, in accordance with  
6           the properties, parameters and/or functions of said electronic circuit arrangement.